

The 8051 Microcontroller Scott Mackenzie

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I. Scott MacKenzie is Associate Professor of Computer Science and Engineering at York University, Canada. For the past 25 years, MacKenzie has been an active member of the

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human-computer interaction (HCI) research community, with over 130 peer-reviewed publications, including more than 30 papers in the Association for Computing Machinery Conference on Human Factors in Computing Systems (ACM SIGCHI

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The 8051 cycle frequency is $1/12$ th the crystal frequency, which, for this problem, is $16 / 12 = 1.33$ MHz. The cycle period is the reciprocal of the cycle frequency, or $1 / 1.33 = 0.75$ μ s. Since the instruction in this problem is a two-cycle instruction, it takes $2 \times 0.75 = 1.5$ μ s to execute. 12.

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Mobility, Accessibility, Universality (2007)

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